L Number	Hits	Search Text	DB	Time stamp
1	76873	time adj constant	USPAT; US-PGPUB; EPO; JPO	2004/02/03 15:23
2	0	(time adj constant) with 5471665.pn.	USPAT; US-PGPUB; EPO; JPO	2004/02/03 15:47
3	0	response with 5471665.pn.	USPAT; US-PGPUB; EPO; JPO	2004/02/03 15:45
4	36213	frequency adj response	USPAT; US-PGPUB; EPO; JPO	2004/02/03 15:23
5	35	integrat$3 with (time adj constant) with (frequency adj response)	USPAT; US-PGPUB; EPO; JPO	2004/02/03 15:23
6	1	response and 5471665.pn.	USPAT; US-PGPUB; EPO; JPO	2004/02/03 15:45
7	0	(time adj constant) and 5471665.pn.	USPAT; US-PGPUB; EPO; JPO	2004/02/03 15:48

L Number	Hits	Search Text	DB	Time stamp
1	76873	time adj constant	USPAT; US-PGPUB; EPO; JPO	2004/02/03 15:23
2	0	(time adj constant) with 5471665.pn.	USPAT; US-PGPUB; EPO; JPO	2004/02/03 15:21
3	0	response with 5471665.pn.	USPAT; US-PGPUB; EPO; JPO	2004/02/03 15:22
4	36213	frequency adj response	USPAT; US-PGPUB; EPO; JPO	2004/02/03 15:23
5	35	integrat$3 with (time adj constant) with (frequency adj response)	USPAT; US-PGPUB; EPO; JPO	2004/02/03 15:23

US-PAT-NO: 5914633

DOCUMENT-IDENTIFIER: US 5914633 A

TITLE: Method and apparatus for tuning a continuous time
filter

DATE-ISSUED: June 22, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP
CODE COUNTRY			
Comino; Vittorio	Keansburg	NJ	N/A
N/A			
Shulman; Dima David	Marlboro	NJ	N/A
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N/A			

US-CL-CURRENT: 327/553, 327/552 , 330/305 , 333/172

ABSTRACT:

    A tuning circuit for generating a digital code to be used to calibrate
a
capacitor array of the type used in active RC filters is comprised of a
single-slope A/D converter with fixed reference voltages as inputs and an
output value which is dependent on the RC product of a resistor and
capacitor
within the converter. A decoder converts the RC product as measured by
the A/D
converter into a digital code which, when applied to the appropriate
capacitor
array, sets the array capacitance to compensate for the difference
between the
measured RC product and the nominal design value.

9 Claims, 5 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 3

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Claims Text - CLTX (37):
   when said circuit is in the second state, said switch connects said
resistor
to a fourth reference voltage, at least one of said switched capacitors
in said
capacitor array is enabled, and said amplifier, resistor, and said at
least one
enabled capacitor comprise an integrator having an RC time constant

defined by
said resistor and said at least one enabled capacitor, said integrator, pulse
generator, counter, and decoder comprise said integrating tuner, said digital
count indicating the magnitude of the RC time constant, said digital code
indicating which of said plurality of capacitors in said array is to be enabled
when the circuit is in the first state so as to adjust the frequency response
of said active RC filter in accordance with said RC time constant.